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Docket No.: S&ZIO030701

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By: 

Date: September 16, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.	:	6,914,851	Confirmation No:	8707
Patentee	:	Oliver Kiehl		
Issued	:	July 5, 2005		
Title	:	Circuit Element with Timing Control		
Customer No.	:	24131		
Docket No.	:	S&ZIO030701		

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.501

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.501 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,031,118 (Morizot), dated July 9, 1991 and corresponding German Published Non-Prosecuted Patent Application DE 37 22 169 A1, dated January 12, 1989;

US 5,031,118 relates to an adapter for controlling a multiple operating mode monitor. The adapter comprises a memory for storing sets of monitor operating parameters for each of a plurality of operating modes at selectively addressable locations. A processing means is provided for evaluating horizontal and vertical synchronizing signals by measuring the respective periods of the synchronizing signals (see claim 16, for example) from a video source to which the monitor is connected to identify one of a plurality of predetermined operating modes corresponding to the synchronizing signals. The set of operating parameters is selectively addressed and the operation of the monitor is adjusted to correspond to the selected set of operating parameters (claim 1 of US 5,031,118).

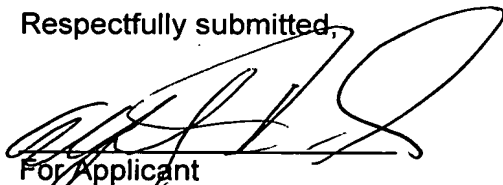
To be more specific, control signals to digital-to-analogue converters 31 to 37 are generated based on the stored values. A horizontal deflection stage 39 and a vertical deflection stage 40 are controlled making use of the analogue-to-digital converter 32 to 37. A video stage 26 is controlled by the analogue-to-digital converter 31. In other words, the analogue-to-digital converter 31 is for controlling brightness and contrast of the video stage 26.

Thus, according to US 5,031,118, the periods of synchronizing signals are measured, while, according to the invention, information representing the clock period duration of an external clock are provided to a circuit element. Thus, US 5,031,118 is not pertinent to claim 1.

Moreover, US 5,031,118 is not pertinent to the subject matter of claim 2, in which it is defined that the unit for the temporal control of the at least one signal on the basis of the information represented in a clock period duration and on nominal time information ascertains a number of clock cycles or fractions of clock cycles of the external clock by which an input signal is to be delayed. US 5,031,118 is silent about ascertaining a number of clock cycles or fractions of clock cycles of an external clock by which an internal signal is to be delayed. In particular, the output of the analog-to-digital converter 31 is for controlling brightness and contrast of the video stage and, therefore, not for determining a delay of the video stage.

Kindly place the references in the Patent Office file wrapper.

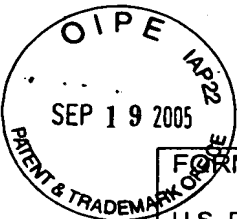
Respectfully submitted,



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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: S&ZIO030701	Applic. No. 10/620,092
	<i>U.S. Patent No. 6,914,851</i>	
	Applicant Oliver Kiehl	
Filing Date July 15, 2003		Group Art Unit 2827

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,031,118	7/1991	Morizot			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J	37 22 169 A1	1/1989	Germany				
	K							
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	
	P	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.